

REMARKS

This Amendment is being filed in response to the Office Action mailed on July 31, 2009 which has been reviewed and carefully considered. Reconsideration and allowance of the present application in view of the amendments made above and the remarks to follow are respectfully requested.

Claims 1-9, 12, 15-16, 18-19 and 20-25 remain in this application, where claims 7-9, 16 and 18-19 had been previously withdrawn, claims 10-11, 13-14 and 17 have been previously canceled without prejudice, and claims 20-25 have been currently added. Claims 1, 12 and 16 are independent.

In the Office Action, claims 1-6 and 10-15 are rejected under 35 U.S.C. §102(b) over U.S. Patent No. 6,292,237 (Hebiguchi) or under 35 U.S.C. §102(b) over Hebiguchi in view of an article entitled "Integrated Driver Circuit for Active Matrix Liquid Crystal Displays" (Reita). Applicant respectfully traverses and submits that claims 1-6, 12, 15 and 20-25, as amended, are patentable over Hebiguchi and Reita for at least the following reasons.

Hebiguchi is directed to an active-matrix substrate for use in an active-matrix liquid-crystal display device of double-scanning-line type. Thin-film transistors and pixel electrodes are arranged on both sides of each data line. A pair of gate lines, between which two adjacent pixels are arranged, is routed to respectively control two adjacent pixels with their respective signals. A storage capacitor of one of the adjacent pixels between adjacent data lines is formed of an overlapping portion of a capacitor electrode in one pixel, with the other gate line being paired with the gate line controlling the one pixel, and the storage capacitor is extended from the one pixel into the other pixel.

It is respectfully submitted that Hebiguchi does not disclose or suggest the present invention as recited in independent claim 1, and similarly recited in independent claim 12 which, amongst other patentable elements, recites (illustrative emphasis provided):

wherein the display elements are arranged in N rows, and wherein a number of transistors of the groups consists of 6N transistors for non-interlaced control of the groups and 7N transistors for interlaced control of the groups.

These features are nowhere disclosed or suggested in Hebiguchi. Rather, Hebiguchi shows in FIG 8 four transistors and

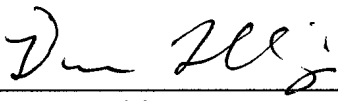
an inverter, and shows in FIG 10 four transistors and four inverters. Reita is cited to allegedly show other features and does not remedy the deficiencies in Hebiguchi.

Accordingly, it is respectfully requested that independent claims 1 and 12 be allowed. In addition, it is respectfully submitted that claims 1-6, 12, 15 and 20-25 should also be allowed at least based on their dependence from independent claims 1 and 12, as well as their individually patentable elements.

In addition, Applicant denies any statement, position or averment of the Examiner that is not specifically addressed by the foregoing argument and response. Any rejections and/or points of argument not addressed would appear to be moot in view of the presented remarks. However, the Applicant reserves the right to submit further arguments in support of the above stated position, should that become necessary. No arguments are waived and none of the Examiner's statements are conceded.

In view of the above, it is respectfully submitted that the present application is in condition for allowance, and a Notice of Allowance is earnestly solicited.

Respectfully submitted,

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October 26, 2009

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